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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/761,253	01/16/2001	Zhenhua Wang	PHCH 000002	5183
24737	7590 05/28/2003			
PHILIPS ELECTRONICS NORTH AMERICAN CORP			EXAMINER	
	WHITE PLAINS RD RRYTOWN, NY 10591		TRAN, TOAN V	
			ART UNIT	PAPER NUMBER
•			2816	
			DATE MAILED: 05/28/2003	

Please find below and/or attached an Office communication concerning this application or proceeding.

.	Application No.	Applicant(s)			
	09/761,253	WANG, ZHENHUA			
Office Action Summary	Examiner	Art Unit			
	Toan Tran	2816			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply					
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). - Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b). Status					
1)⊠ Responsive to communication(s) filed on <u>24 F</u>	ebruary 2003 .				
2a) ☐ This action is FINAL . 2b) ☑ Thi	s action is non-final.				
3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213. Disposition of Claims					
4) Claim(s) <u>1-21</u> is/are pending in the application.					
4a) Of the above claim(s) is/are withdrawn from consideration.					
5)⊠ Claim(s) <u>15-17,19 and 22</u> is/are allowed.					
6)⊠ Claim(s) <u>1,3-5,18 and 21</u> is/are rejected.					
7) Claim(s) <u>2 and 6-14</u> is/are objected to.					
8) Claim(s) are subject to restriction and/or election requirement. Application Papers					
9)☐ The specification is objected to by the Examiner	•				
10)⊠ The drawing(s) filed on <u>16 January 2001</u> is/are: a)⊠ accepted or b)⊡ objected to by the Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
11)☐ The proposed drawing correction filed on is: a)☐ approved b)☐ disapproved by the Examiner.					
If approved, corrected drawings are required in reply to this Office action.					
12) ☐ The oath or declaration is objected to by the Examiner.					
Priority under 35 U.S.C. §§ 119 and 120					
13)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).					
a)⊠ All b)□ Some * c)□ None of:					
1. Certified copies of the priority documents	have been received.				
2. Certified copies of the priority documents	have been received in Application	on No			
 Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 					
14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).					
a) The translation of the foreign language provisional application has been received. 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.					
Attachment(s)					
Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449) Paper No(s) Hotore of Informal Patent Application (PTO-152) Other:					
S. Patent and Trademark Office					

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DETAILED ACTION

1. This is in response to the Amendment filed February 24, 2003. The amendment and arguments have been fully reviewed and are persuasive to overcome the rejections in the previous Office action. However, some claims are now rejected in view of a new ground of rejection.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1, 3-5, 18, and 21 are rejected under 35 U.S.C. 102(b) as being anticipated by Lim (US Patent 5,319,265).

Regarding claim 1, Lim discloses in Fig. 2 a voltage level monitoring circuit comprising: a first reference current (the current flowing through RB); a second reference current (IB); a controllable switch (SW1); a monitoring current (the current flowing through RA) derived from a voltage (VIN) to be measured; a comparator device (u1) including a first current input (+ input) coupled for receiving the first reference current (the current through RB) in response to the controllable switch being non-conductive; and for receiving both the first and the second currents (the current through RB and IB) in response to the controllable switch being conductive; a second input (- input) for receiving the monitoring current; and at least one measuring signal output (Vout); the comparator device (u1) is arranged for comparing the currents received at

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the inputs and for generating at the output a measuring signal with a first value when the current received at its second input is less than that at the first input, and with a second value when the current at the second input is more than that at the first input.

Regarding claim 3, the current output of the second reference current source (IB) is coupled to the comparator device through the controllable switch (SW1).

Regarding claim 4, Fig. 2 shows that the switch (SW1) is controlled by a control signal generated by the comparator device (u1).

Regarding claim 5, the control signal (output of u1) renders the switch conductive when the current at the first input (+ input) is higher than that at the second input (- input) and nonconductive when the current at the first input is less than that of the second input.

Claims 18 and 21 are rejected for the same reasons set forth in the rejections to claims 1, 3 and 5 above.

Allowable Subject Matter

4. Claims 2, 6-19 and 22 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Reasons: Claim 2 would be allowable because the prior art does not teach or suggest the first reference current source include a PMOS transistor (50 in Fig. 2) connected as recited in the claim in combination with the rest of limitations in the claim. Claim 6 would be allowable because the prior art does not teach or suggest the second reference current source include the PMOS transistor (60 in Fig. 2) connected as

recited in the claim. Claim 7 would be allowable because the prior art does not teach or suggest the controllable switch (70 in Fig. 2) as recited in the claim. Claims 8-9, 19, and 22 contain allowable subject matter because the prior art does not teach or suggest the two inverters (80, 83) as recited in the claims. Claims 10-14 would be allowable because the prior art does not teach or suggest details of the monitoring current source (41, 42, 49 in Fig. 2) as recited in the claims. Claims 15-17 would be allowable because the prior art does not teach or suggest the current source being "programmable."

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Toan Tran whose telephone number is (703) 308-4866. The examiner can normally be reached on 8:30am-6pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan can be reached on (703) 308-4876. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 872-9318 for regular communications and (703) 872-9319 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956. Toan Lan

> Toan Tran **Primary Examiner** Art Unit 2816